Exercise 1 Check the Eventual Access property in modal logic for the systems from Exercises1 (Sep 03), the one with one $t_1t_2$ state and the other one with two $t_1t_2$ states.

Exercise 2 Check the Eventual Access property in CTL for the systems from Exercise1, the one with one $t_1t_2$ state and the other one with two $t_1t_2$ states.

Exercise 3 Check the Non-blocking property in CTL for the same systems.

Exercise 4 Consider the following model:

For each state of this model check the validity of the CTL formulas: a) $EF(AGp)$ b) $A[p U EG(p \Rightarrow q)]$ and c) $E[(p \land q) \lor r) U (r U AGp)]$ using the labeling algorithm.

Exercise 5 Consider the following model:

For each state of this model, check the validity of the CTL formula: $AF(p \Rightarrow AFq)$ using the labeling algorithm.