

# Factorization in Process Domains (Second Approach)

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In this note we present a general theory about factorization in process domains.

## 1 Introduction

By way of introduction, consider a process domain  $P$  together with a binary relation  $\sqsubseteq$  (refinement relation:  $p \sqsubseteq q$  expresses ‘ $q$  refines  $p$ ’<sup>1</sup>) and a binary operator  $\parallel$  (parallel composition). We assume that  $\langle P, \sqsubseteq \rangle$  is a complete poset, in the sense that every subset has a greatest lower bound. Furthermore, we assume that  $\parallel$  has unit  $e$  and is commutative, associative, and (universally)  $\sqcap$ -junctive. The latter means that for process  $q$  and set of processes  $V$  we have

$$(\sqcap V) \parallel q = (\sqcap p : p \in V : p \parallel q). \quad (1)$$

Finally, we assume that refinement is **fully abstract** with respect to a form of **testing**, that is, there exists a process  $d$  such that

$$p \sqsubseteq q \equiv (\forall r : p \parallel r \sqsupseteq d : q \parallel r \sqsupseteq d). \quad (2)$$

One may interpret

$$p \parallel r \sqsupseteq d$$

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<sup>1</sup>Often,  $p \sqsubseteq q$  corresponds to ‘ $q$  is more deterministic than  $p$ ’.

as ‘process  $p$  passes the test in the context of process  $r$ ’. An immediate consequence of (2) is

$$p = q \equiv (\forall r :: p \parallel r \sqsupseteq d \equiv q \parallel r \sqsupseteq d), \quad (3)$$

that is, two processes are equal if and only if they pass the same tests. It the implication from right to left that is important for full abstraction.

We are interested in the **design equation**

$$p :: p \parallel q \sqsupseteq r \quad (4)$$

for given processes  $q$  and  $r$ . The equation arises when one has decided to implement specification  $r$  as the parallel composition of process  $q$  with some yet unknown process  $p$ . In particular, we would like to know the  $\sqsubseteq$ -least solution to the design equation, which—if it exists—could serve as specification for  $p$ .

## 2 Theory

We define the **reflection**  $\sim q$  of process  $q$  by

$$\sim q = (\sqcap p : p \parallel q \sqsupseteq d : p). \quad (5)$$

It is a proper definition because all glb’s exist. Notationally, reflection binds stronger than parallel composition. According to Corollary 2.3 below, the reflection of  $q$  is the  $\sqsubseteq$ -least solution to a very particular design equation, viz.  $p :: p \parallel q \sqsupseteq d$ . The reflection of  $q$  can

also be interpreted as the severest test that  $q$  passes. In Corollary 2.9 we shall see that the general design equation can be solved in terms of reflection and parallel composition.

First of all we show that  $\sim q$  is a solution to the design equation  $p :: p \parallel q \sqsupseteq d$ .

**2.1 Property** For process  $q$  we have

$$\sim q \parallel q \sqsupseteq d.$$

**Proof** We derive for process  $q$

$$\begin{aligned} & \sim q \parallel q \\ \equiv & \{ \text{definition of reflection: (2)} \} \\ & (\sqcap p : p \parallel q \sqsupseteq d : p) \parallel q \\ \equiv & \{ \text{parallel composition is } \sqcap\text{-junctive: (1)} \} \\ & (\sqcap p : p \parallel q \sqsupseteq d : p \parallel q) \\ \sqsupseteq & \{ \text{property of greatest lower bound} \} \\ & d \end{aligned}$$

The next property turns out to be a very useful characterization of reflection. In the remainder of this note, it will be used instead of the definition of reflection.

**2.2 Property** For processes  $p$  and  $q$  we have

$$p \parallel q \sqsupseteq d \equiv p \sqsupseteq \sim q.$$

**Proof** We derive the implication from left to right

$$\begin{aligned} & p \parallel q \sqsupseteq d \\ \Rightarrow & \{ \text{property of greatest lower bound} \} \\ & p \sqsupseteq (\sqcap p : p \parallel q \sqsupseteq d : p) \\ \equiv & \{ \text{definition of reflection: (5)} \} \\ & p \sqsupseteq \sim q \end{aligned}$$

and the implication from right to left

$$\begin{aligned} & p \sqsupseteq \sim q \\ \equiv & \{ \text{full abstraction of refinement: (2)} \} \\ & (\forall r : \sim q \parallel r \sqsupseteq d : p \parallel r \sqsupseteq d) \\ \Rightarrow & \{ \text{instantiation with } r := q, \text{ using Prop-} \\ & \text{erty 2.1} \} \end{aligned}$$

$$p \parallel q \sqsupseteq d$$

■

**2.3 Corollary** The set of solutions to the design equation  $p :: p \parallel q \sqsupseteq d$  is  $\sqsubseteq$ -upward closed and the  $\sqsubseteq$ -least solution is given by  $\sim q$ . ■

It turns out that  $d$  is the reflection of the unit  $e$ , irrespective of which process  $d$  actually is. Note, however, that the choice of  $d$  is limited by (2) and that reflection depends on  $d$ .

**2.4 Property** We have

$$\sim e = d.$$

**Proof** We derive for arbitrary process  $r$

$$\begin{aligned} & r \sqsupseteq \sim e \\ \equiv & \{ \text{Property 2.2} \} \\ & r \parallel e \sqsupseteq d \\ \equiv & \{ e \text{ is unit of parallel composition} \} \\ & r \sqsupseteq d \end{aligned}$$

■

On account of

$$p = q \equiv (\forall r : r \sqsupseteq p \equiv r \sqsupseteq q), \quad (6)$$

we now infer  $\sim e = d$ . ■

Reflection reverses the order.

**2.5 Property** For processes  $p$  and  $q$  we have

$$p \sqsubseteq q \equiv \sim p \sqsupseteq \sim q.$$

**Proof** For processes  $p$  and  $q$  we derive

$$\begin{aligned} & p \sqsubseteq q \\ \equiv & \{ \text{refinement is fully abstract: (2)} \} \\ & (\forall r : p \parallel r \sqsupseteq d : q \parallel r \sqsupseteq d) \\ \equiv & \{ \text{parallel composition is commutative} \} \\ & (\forall r : r \parallel p \sqsupseteq d : r \parallel q \sqsupseteq d) \\ \equiv & \{ \text{Property 2.2} \} \\ & (\forall r : r \sqsupseteq \sim p : r \sqsupseteq \sim q) \\ \equiv & \{ \text{property of partial ordering} \} \end{aligned}$$

$$\sim p \sqsupseteq \sim q$$

The next property is a slight modification of Property 2.2.

**2.6 Property** For processes  $p$  and  $q$  we have

$$p \sqsupseteq q \equiv p \parallel \sim q \sqsupseteq d.$$

**Proof** We derive for processes  $p$  and  $q$

$$\begin{aligned} & p \sqsupseteq q \\ \equiv & \{ \text{Property 2.5} \} \\ & \sim q \sqsupseteq \sim p \\ \equiv & \{ \text{Property 2.2, using commutativity of } \parallel \} \\ & p \parallel \sim q \sqsupseteq d \end{aligned}$$

Reflection is an involution, that is, its own inverse.

**2.7 Property** For process  $p$  we have

$$\sim \sim p = p.$$

**Proof** We derive for processes  $p$  and  $r$

$$\begin{aligned} & r \sqsupseteq \sim \sim p \\ \equiv & \{ \text{Property 2.2} \} \\ & r \parallel \sim p \sqsupseteq d \\ \equiv & \{ \text{Property 2.6} \} \\ & r \sqsupseteq p \end{aligned}$$

On account of (6), we now infer  $\sim \sim p = p$ .

Finally, we can derive an elegant expression for the  $\sqsupseteq$ -least solution to the design equation (4).

**2.8 Theorem** For processes  $p$ ,  $q$ , and  $r$  we have

$$p \parallel q \sqsupseteq r \equiv p \sqsupseteq \sim(q \parallel \sim r).$$

**Proof** For processes  $p$ ,  $q$ , and  $r$  we derive

$$\begin{aligned} & p \parallel q \sqsupseteq r \\ \equiv & \{ \text{Property 2.6} \} \\ & (p \parallel q) \parallel \sim r \sqsupseteq d \\ \equiv & \{ \text{parallel composition is associative} \} \end{aligned}$$

$$p \parallel (q \parallel \sim r) \sqsupseteq d$$

$$\equiv \{ \text{Property 2.2} \}$$

$$p \sqsupseteq \sim(q \parallel \sim r)$$

**2.9 Corollary** The  $\sqsupseteq$ -least solution to the design equation (4) is

$$\sim(q \parallel \sim r). \quad (7)$$

A common notation for the  $\sqsupseteq$ -least solution to the design equation (4) is  $r/q$ . The operator  $/$  on processes is completely characterized by the equivalence

$$p \parallel q \sqsupseteq r \equiv p \sqsupseteq r/q. \quad (8)$$

- Using this notation, reflection can be expressed by  $\sim q = d/q$ . It is often more convenient to manipulate  $/$  using (8) than to manipulate the fairly awkward definition of  $/$  in terms of  $\sim$  and  $\parallel$ .

### 3 Additional Results

Note that in the preceding section we have used the  $\sqsupseteq$ -junctivity of parallel composition only once, viz. in Property 2.1, where it is used to distribute parallel composition over the greatest lower bound of a very particular set of processes. Hence, to obtain all results, in particular Theorem 2.8, it suffices to assume Property 2.1 instead of the  $\sqsupseteq$ -junctivity of  $\parallel$ . But  $\sqsupseteq$ -junctivity of  $\parallel$  cannot be denied, because it follows from Theorem 2.8:

**Proof** Let  $q$  be a process and  $V$  a set of processes. We derive for process  $r$

$$\begin{aligned} & (\sqcap V) \parallel q \sqsupseteq r \\ \equiv & \{ \text{Theorem 2.8} \} \\ & \sqcap V \sqsupseteq \sim(q \parallel \sim r) \\ \equiv & \{ \text{property of greatest lower bound} \} \\ & (\forall p : p \in V : p \sqsupseteq \sim(q \parallel \sim r)) \\ \equiv & \{ \text{Theorem 2.8} \} \\ & (\forall p : p \in V : p \parallel q \sqsupseteq r) \end{aligned}$$

$$\equiv \{ \text{property of greatest lower bound} \}$$

$$(\sqcap p : p \in V : p \parallel q) \sqsupseteq r$$

On account of (6), we then have

$$p \parallel \sqcap V = (\sqcap q : q \in V : p \parallel q). \quad (9)$$

■

Finally, we observe that, since  $\langle P, \sqsubseteq \rangle$  is self-dual (under  $\smile$ ), it is not only a complete poset but, in fact, a complete lattice, in the sense that every subset has a greatest lower bound and a least upper bound. Actually, the completeness assumption is too strong. We only used the existence of greatest lower bounds of sets like

$$\{r : p \parallel r \sqsupseteq d : r\},$$

for instance, in the definition of reflection.

## 4 Conclusion

We have shown that under certain conditions (especially full abstraction: (2)) the design equation over a process domain has an elegant solution in terms of a reflection operator. In various domains, the reflection operator is very simple. Of course, our presentation raises new questions. These will be addressed elsewhere.

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